

PENTING / IMPORTANT:

Kandungan Pro Forma ini tidak boleh diubah tanpa kelulusan Senat bagi perkara-perkara yang telah ditandakan*. Pindaan kepada perkara lain boleh diluluskan di peringkat Akademi/Fakulti/Institut/Pusat.

*Contents of this Pro Forma shall not be changed without the Senate's approval for items indicated with *. Changes to the other items can be approved at the Academy/Faculty/Institution/Centre level.*

	Versi Bahasa Malaysia Malay Version	Versi Bahasa Inggeris English Version
Akademi/Fakulti/Institut/Pusat <i>Academy/Faculty/Institute/Centre</i>	Fakulti Kejuruteraan	<i>Faculty of Engineering</i>
Jabatan <i>Department</i>	Jabatan Kejuruteraan Elektrik	<i>Department of Electrical Engineering</i>
Nama Program Akademik <i>Name of Academic Programme</i>	Sarjana Muda Kejuruteraan Elektrik	<i>Bachelor of Electrical Engineering</i>
Kod Kursus* <i>Course Code*</i>	KIE2005	<i>KIE2005</i>
Tajuk Kursus* <i>Course Title*</i>	Analisa Litar II	<i>Circuit Analysis II</i>
Kredit* <i>Credit*</i>	3	3
Masa Pembelajaran Pelajar (SLT) <i>Student Learning Time (SLT)</i>	120	120
Prasyarat/Keperluan Minimum Kursus <i>Course Pre-requisite(s)/Minimum Requirement(s)</i>	Tiada	<i>None</i>
Hasil Pembelajaran Kursus* <i>Course Learning Outcomes*</i>	Pada akhir kursus ini, pelajar dapat: <ol style="list-style-type: none"> 1) Mengaplikasikan teknik-teknik analisa litar dan transformasi lanjutan untuk kedua-dua litar arus terus (DC) dan arus ulang-alik (AC). 2) Merekabentuk litar-litar khusus untuk melaksanakan tugas-tugas kejuruteraan sebenar. 3) Membina pelbagai jenis litar dari komponen asas 	<i>At the end of the course, students are able to:</i> <ol style="list-style-type: none"> 1) <i>Apply advanced circuit analysis and transformation techniques to both direct current (DC) and alternating current (AC) circuits.</i> 2) <i>Design specific circuits to perform real-life engineering tasks.</i>

	Versi Bahasa Malaysia Malay Version	Versi Bahasa Inggeris English Version
	dalam kumpulan kecil untuk menunjukkan aplikasinya.	3) <i>Construct various circuits from basic components in small group to demonstrate their application.</i>
Kemahiran Insaniah <i>Soft Skills</i>	Pemikiran Kritis dan Penyelesaian Masalah (CT1-CT3)	<i>Critical Thinking and Problem Solving Skills (CT1-CT3)</i>
Sinopsis Kandungan Kursus <i>Synopsis of Course Contents</i>	Graf aliran isyarat, Transformasi Laplace, rangkaian dua-pengkalan, padanan impedan, transformasi rangkaian, teori realisabiliti, fungsi hakiki positif, teori hampiran, sintesis Darlington, rekabentuk turas pasif dan aktif	<i>Laplace transform in circuit analysis, Signal Flow Graph Circuit Analysis Techniques, frequency selective circuits, active Filter circuits, Fourier series, Fourier transform, two-port circuits, Attenuator design, Impedance Matching and Network Transformation</i>
Pemberatan Penilaian* <i>Assessment Weightage*</i>	Penilaian Berterusan: 40% Peperiksaan Akhir: 60%	<i>Continuous Assessment: 40% Final Examination: 60%</i>
Kaedah Maklum Balas Tentang Prestasi <i>Methodologies for Feedback on Performance</i>	Gred/markah untuk tugas, ujian dan/atau perbincangan berkumpulan diumumkan dalam kelas dan/atau dipamerkan di papan kenyataan.	<i>Grades/marks for assignment, test and/or group discussion announced in class and/or displayed on the notice board</i>
Kriteria Dalam Penilaian Sumatif <i>Criteria in Summative Assessment</i>	Sila rujuk Kaedah-Kaedah Universiti Malaya (Pengajian Ijazah Pertama) 2019 dan Peraturan-Peraturan Universiti Malaya (Pengajian Ijazah Pertama) 2019	<i>Please refer to the University Of Malaya (First Degree Studies) Rules 2019 And University Of Malaya (First Degree Studies) Regulations 2019</i>