

PENTING / IMPORTANT:

Kandungan Pro Forma ini tidak boleh diubah tanpa kelulusan Senat bagi perkara-perkara yang telah ditandakan*. Pindaan kepada perkara lain boleh diluluskan di peringkat Akademi/Fakulti/Institut/Pusat.

*Contents of this Pro Forma shall not be changed without the Senate's approval for items indicated with *. Changes to the other items can be approved at the Academy/Faculty/Institution/Centre level.*

	Versi Bahasa Malaysia Malay Version	Versi Bahasa Inggeris English Version
Akademi/Fakulti/Institut/Pusat <i>Academy/Faculty/Institute/Centre</i>	Fakulti Kejuruteraan	<i>Faculty of Engineering</i>
Jabatan <i>Department</i>	Jabatan Kejuruteraan Elektrik	<i>Department of Electrical Engineering</i>
Nama Program Akademik <i>Name of Academic Programme</i>	Sarjana Muka Kejuruteraan (Elektrik)	<i>Bachelor of Engineering (Electrical)</i>
Kod Kursus* <i>Course Code*</i>	KIE4023	<i>KIE4023</i>
Tajuk Kursus* <i>Course Title*</i>	Sistem Kawalan Digital	<i>Digital Control System</i>
Kredit* <i>Credit*</i>	2	<i>2</i>
Masa Pembelajaran Pelajar (SLT) <i>Student Learning Time (SLT)</i>	80	<i>80</i>
Prasyarat/Keperluan Minimum Kursus <i>Course Pre-requisite(s)/Minimum Requirement(s)</i>	Tiada	<i>No</i>
Hasil Pembelajaran Kursus* <i>Course Learning Outcomes*</i>	Di akhir kursus ini, pelajar dapat: <ol style="list-style-type: none"> 1) Menerangkan ciri asas gelung terbuka dan tertutup bagi system diskrit. 2) Membina perwakilan sistem menggunakan persamaan kebezaan, rangkap pindah dan model keadaan-ruang. 3) Menganalisa dan keupayaan dan kestabilan 	<i>At the end of the course, students are able to:</i> <ol style="list-style-type: none"> 1) <i>Explain basic features of open and closed loop discrete systems</i> 2) <i>Construct systems representation using difference equations, transfer function and state space models</i> 3) <i>Analyze digital system performance and</i>

	Versi Bahasa Malaysia Malay Version	Versi Bahasa Inggeris English Version
	sistem digital.	<i>stability</i>
Kemahiran Insaniah <i>Soft Skills</i>	Kemahiran Pemikiran Kritis dan Penyelesaian Masalah (CT1-CT3)	<i>Critical Thinking and Problem Solving Skills (CT1-CT3)</i>
Sinopsis Kandungan Kursus <i>Synopsis of Course Contents</i>	Kursus ini bertujuan mendedahkan pelajar kepada teori asas sistem kawalan digital.	<i>The aim of this course is to expose students to the basic theory of digital control systems.</i>
Pemberatan Penilaian* <i>Assessment Weightage*</i>	Penilaian Berterusan: 40% Peperiksaan Akhir: 60%	<i>Continuous Assessment: 40% Final Examination: 60%</i>
Kaedah Maklum Balas Tentang Prestasi <i>Methodologies for Feedback on Performance</i>	Maklumbalas secara dalam talian dan komen secara lisan semasa seminar.	<i>Online feedback and oral comments after Seminar.</i>
Kriteria Dalam Penilaian Sumatif <i>Criteria in Summative Assessment</i>	Sila rujuk Kaedah-Kaedah Universiti Malaya (Pengajian Ijazah Pertama) 2017 dan Peraturan-Peraturan Universiti Malaya (Pengajian Ijazah Pertama) 2017	<i>Please refer to the University Of Malaya (First Degree Studies) Rules 2017 And University Of Malaya (First Degree Studies) Regulations 2017</i>